

FIG. 1

ADAPTIVE BIT RATE TRANSPONDER

Inventors Jing W. Jiang, Xiaojun Fang, Robert J. Deri

and Lawrence J. Pelz

Docket No., 21660-05933

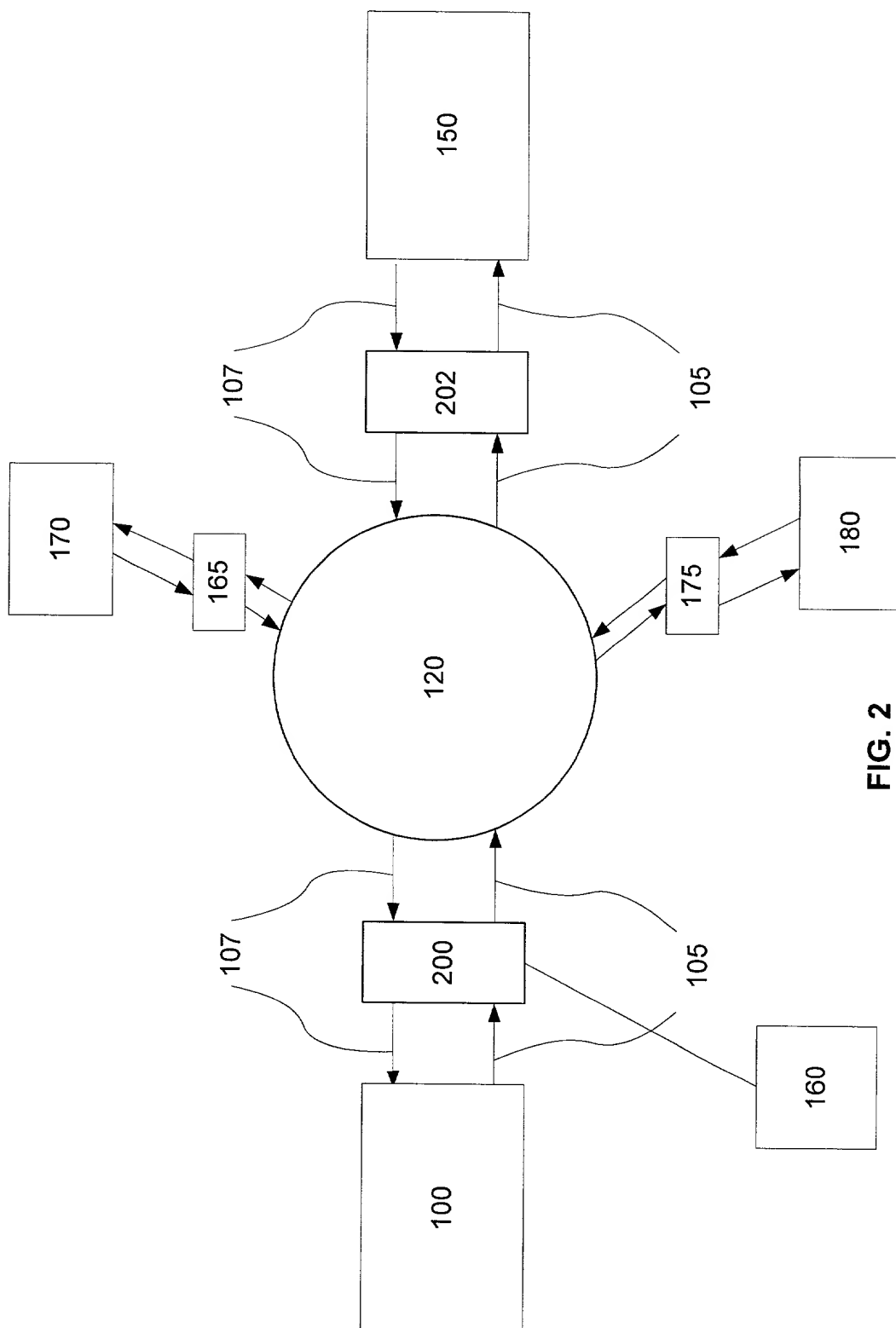


FIG. 2

ADAPTIVE BIT RATE TRANSPONDER

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and Lawrence J. Pelz

Docket No.: 21680-05933

FIG. 3A is a block diagram of an adaptive bit rate transponder 200. The transponder 200 includes a framing device 230, a first receiver 210-1, a first transmitter 225-1, a second receiver 210-2, a second transmitter 225-2, a first clock divider 215-1, a second clock divider 215-2, a first mixer 220-1, a second mixer 220-2, a first local oscillator 217-1, and a CPU 205. The transponder 200 is configured to receive a signal 105 and transmit a signal 107.

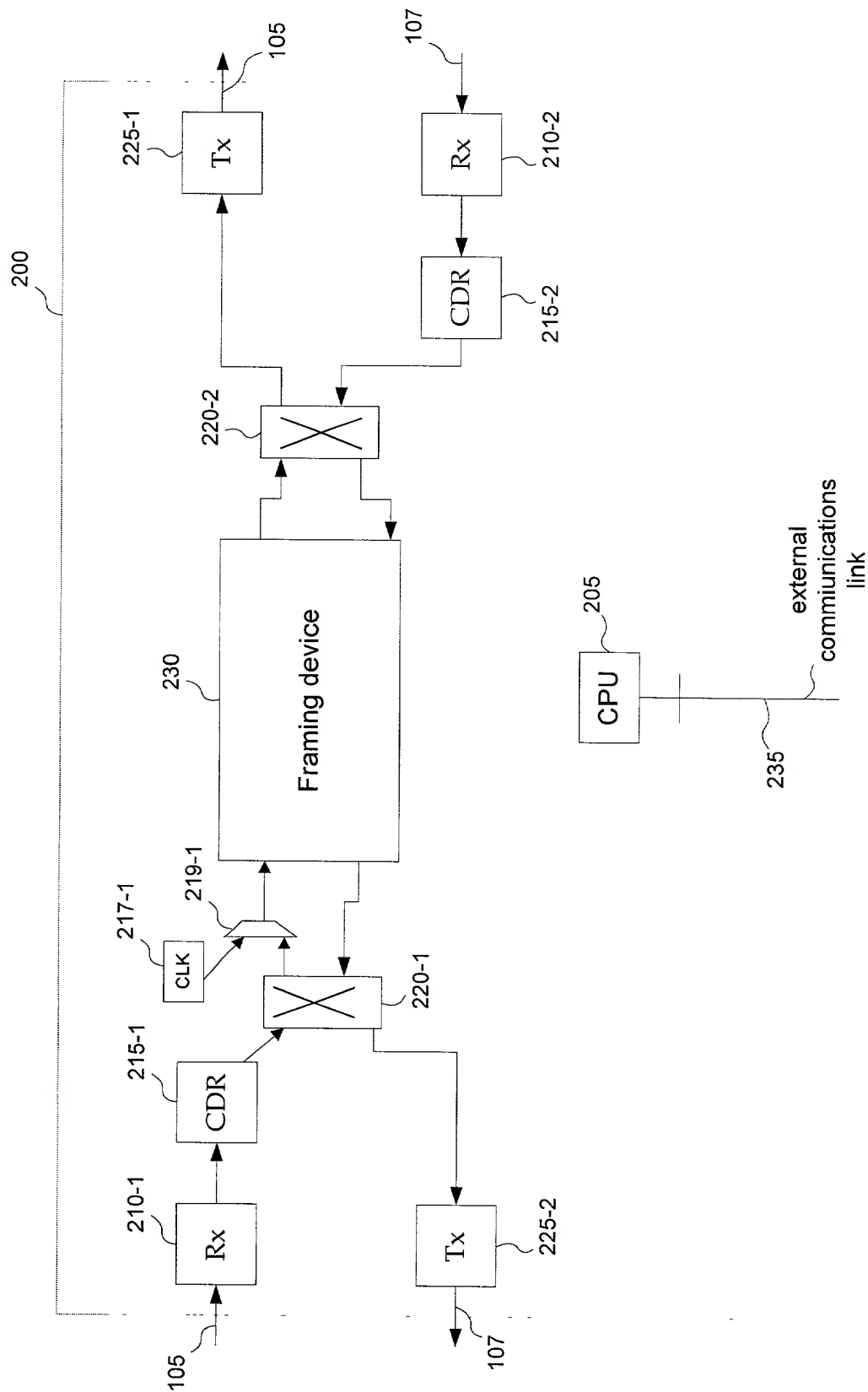


FIG. 3A

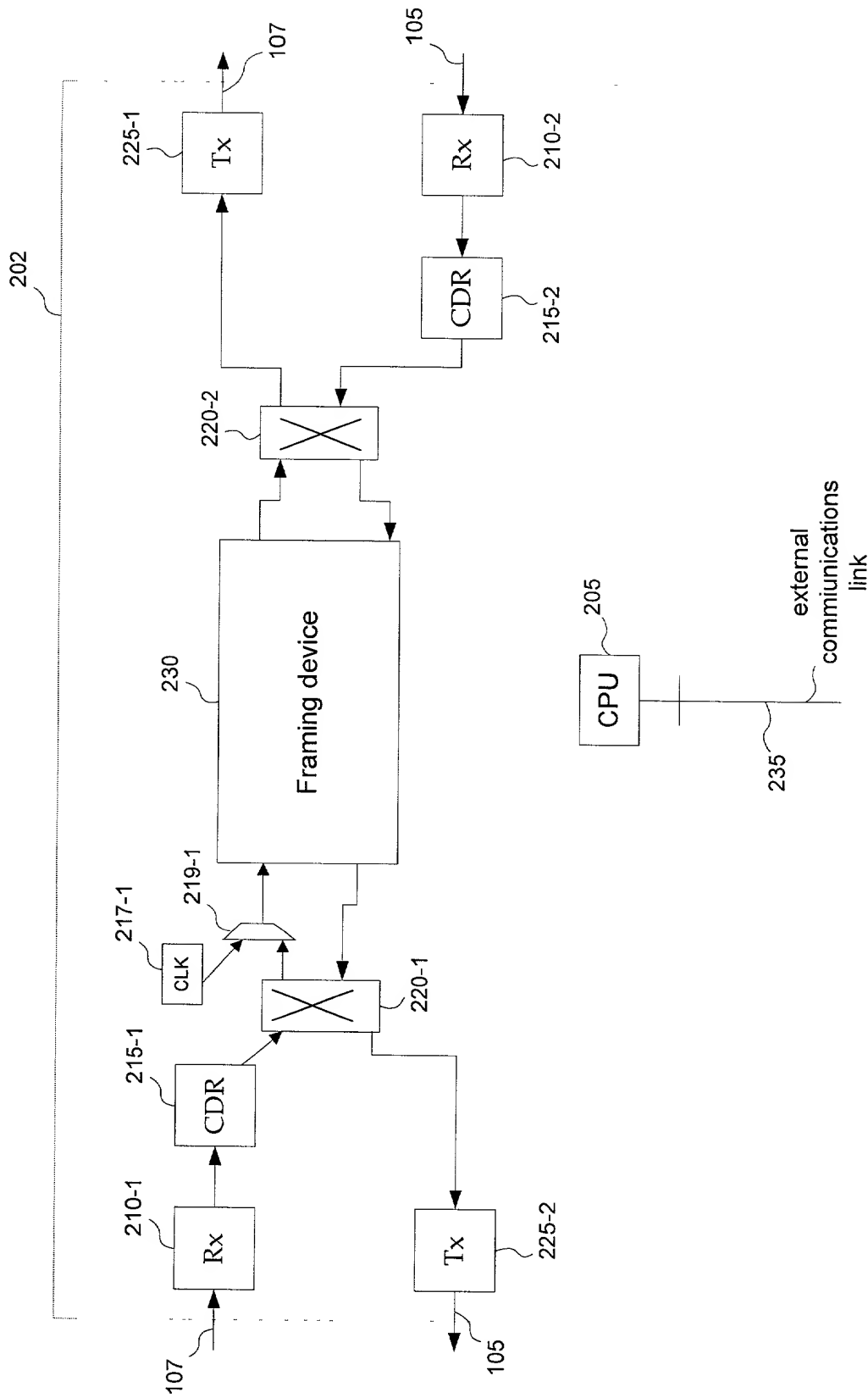


FIG. 3B

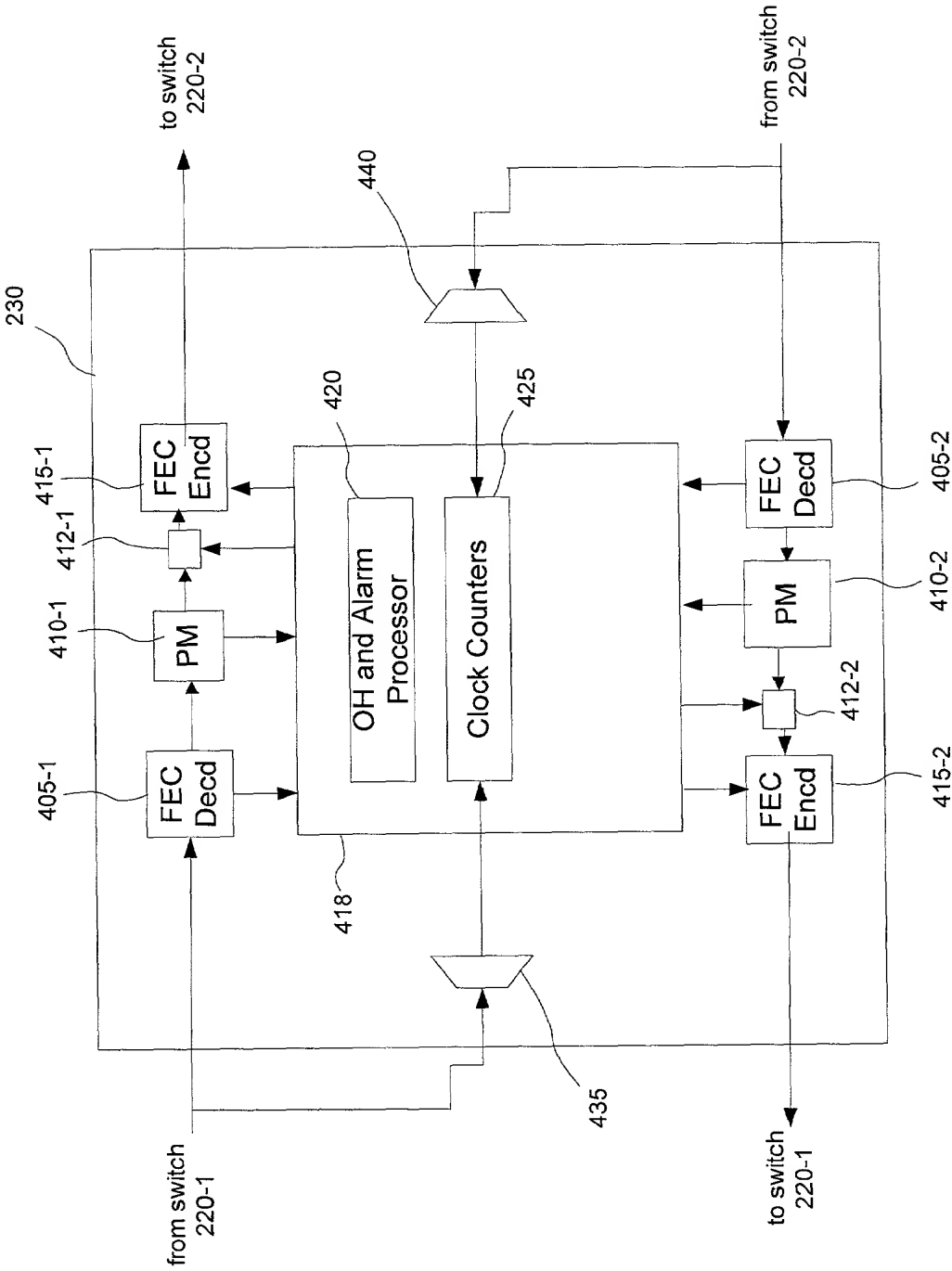


FIG. 4

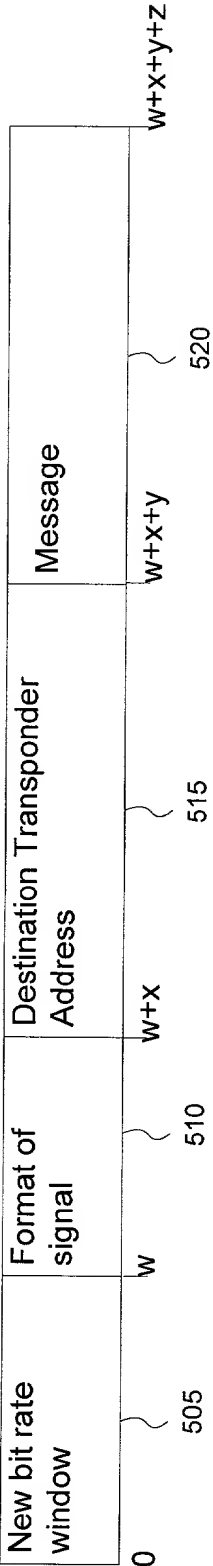


FIG. 5

FIG. 6

